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HSK400X Series

1.0AMP. SURFACE MOUNT RECTIFIERS

Features

- Low forward surge current
- Ideal for surface mouted applications
- Low leakage current

Mechanical Data

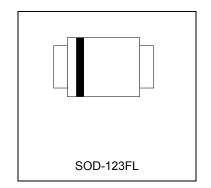
- Case:JEDEC SOD-123FL,molded plastic over passivated chip.
- Terminals:Solder Plated, solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Weight: 0.006 ounces, 0.02 gram .
- Mounting position: Any.

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

or inductive load. For capacitive load, derate c	Jantoni	<i>Dy 20</i>	,,,						
		HSK	HSK	HSK	HSK	HSK	HSK	HSK	UNITS
		4001	4002	4003	4004	4005	4006	4007	
Device marking code		D1	D2	D3	D4	D5	D6	D7	
Maximum recurrent peak reverse voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS voltage	VRMS	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	VDC	50	100	200	400	600	800	1000	V
Maximum average forword rectified current @Ta=75℃	I _(AV)	1						Α	
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load	I _{FSM}	25						Α	
Maximum DC reverse current @T _A =25℃ at rated DC blocking voltage @TA=125℃	lr	5.0							μA
	IR	50.0							
measured at f=1MHz,V _R =4.0V Typical junction capacitance	Сл	15							рF
Typical thermal resistance junction to lead	Rejl	20							°C/W
Operating temperature range	Tj	- 55 + 150							$^{\circ}\mathbb{C}$
Storage temperature range	Тѕтс	- 55 + 150							$^{\circ}\!\mathbb{C}$
N (4 D) (1) (1) (10) (10)	1 00/								1

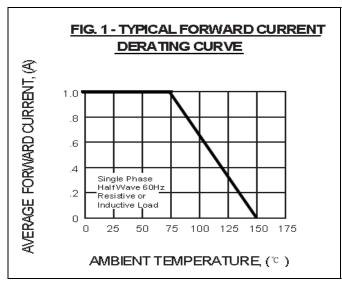
Note1: Pulse test: pulse width 300µsec,duty cycle 2%.

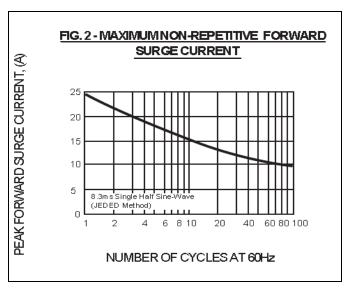


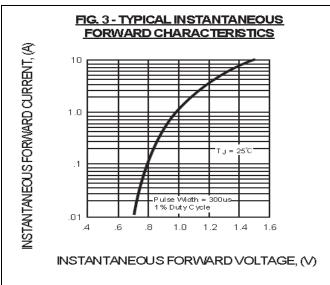
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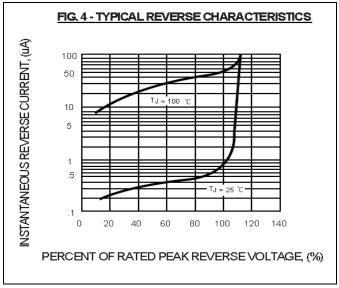
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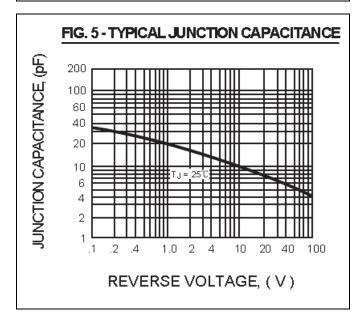
Characteristics Curve







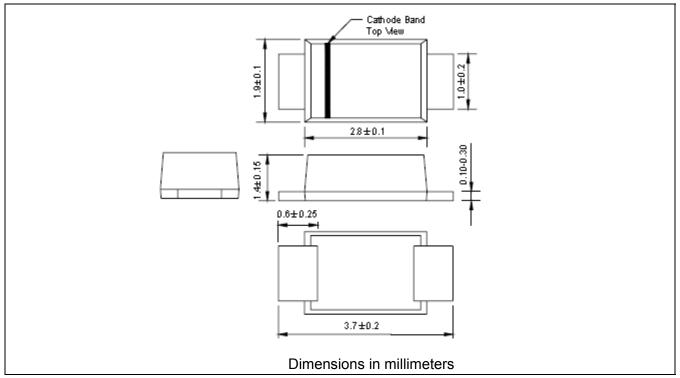




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SOD-123FL Dimension



*:Typical

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